

What is claimed is

1. A method of preventing the external detection of
5 operations in a digital integrated circuit comprising an asynchronous circuit,

comprising the method step of time-varying a supply voltage
of said asynchronous circuit to time-shift the execution
10 time of operations within said asynchronous circuit.
2. The method according to claim 1, wherein the time
variation of said supply voltage takes place in a random
way.
- 15 3. A digital integrated circuit comprising:

an asynchronous circuit, and

20 means for time-varying a supply voltage of said asynchronous circuit to time-shift the execution point of operations within said asynchronous circuit.
4. The digital integrated circuit according to claim 3,
25 wherein said means for time-varying said supply voltage comprises a random number generator.
5. The digital integrated circuit according to claim 4,
wherein said means for time-varying said supply voltage
30 further comprises a noise voltage source driving said random-number generator.
6. The digital integrated circuit according to claim 4,
wherein said means for time-varying said supply voltage
35 further comprises a digital-analog converter transforming the digital values produced by said random-number generator into an analog voltage.

7. The digital integrated circuit according to claim 3, wherein said means for time-varying said supply voltage further comprises a voltage regulator.

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8. The digital integrated circuit according to claim 3, wherein said asynchronous circuit is formed for executing a coding algorithm.